

ABSTRACT OF THE DISCLOSURE

A method of erasing a logical data block of a MRAM according to an embodiment of the present invention is disclosed. The method comprises providing a MRAM having a logical data block configured for a distribution of selected and unselected write field thresholds when switching from a logical one state to a logical zero state, wherein the selected write field threshold is separated from the unselected write field threshold by a preselected amount and writing all bits of the logical data block to the logical zero state.

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